IN THE SPECIFICATION:

Kindly replace the first full paragraph on page 4 with the following:

The embodiment of the tuner IC embodiment shown in the figure has a first selectivity filter (SF1) between the amplifiers A2 and A3, a mixer M between the amplifiers A3 and A4, and a second selectivity filter SF2 between the amplifiers A4 and A5. Advantageously, the input of the RSSI circuit is coupled to the output of the second selectivity filter SF2, so that its measurement is not disturbed by irrelevant frequencies. Advantageously, the input of the RSSI filter is coupled to the output of the mixer M (preferably but not necessarily thru the second selectivity filter SF2), so that the RSSI circuit input is at a relatively low frequency. The mixer M converting from RF to low-IF is quite advantageous to enable narrow filtering inside the channel bandwidth. The narrow filter NF preferably selects 1-2 MHz from a 5-6 MHz bandwidth channel. The power inside the narrow spectrum of the wanted signal should relate to the wanted signal power; this condition is fulfilled with e.g. OFDM and QAM signals. Advantageously, only discretely controlled amplifiers A1-A4 are present in the path between in the tuner IC input and the RSSI circuit input, so that there is a clear relation between control voltage and amplification that does not change with time and/or in dependence on the temperature, so that an accurate calibration is possible.